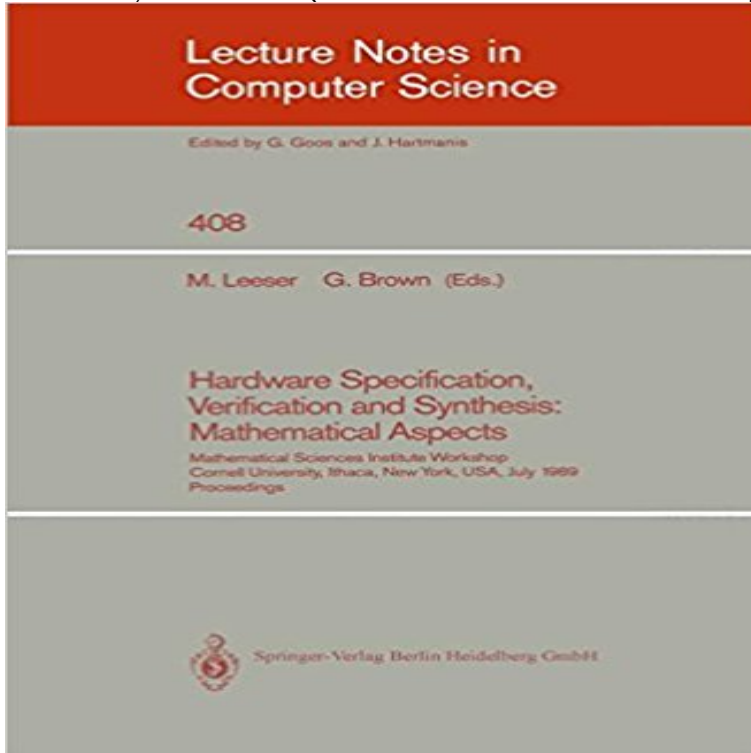


Hardware Specification, Verification and Synthesis: Mathematical Aspects: Mathematical Sciences Institute Workshop. Cornell University Ithaca, New ... (Lecture Notes in Computer Science)



Current research into formal methods for hardware design is presented in the papers in this volume. Because of the complexity of VLSI circuits, assuring design validity before circuits are manufactured is imperative. The goal of research in this area is to develop methods of improving the design process and the quality of the resulting designs. The major trend apparent at the workshop is that researchers are rapidly moving away from post hoc proof techniques with their great expense. A number of papers were presented that dealt with problems of synthesizing correct circuits and of designing with the goal of verification. Researchers are also beginning to deal with the theoretical issues of reasoning about concurrent systems and asynchronous systems, and to introduce new logical tools such as constructive type theory and category theory. Most of the research reported was performed in the United States.

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Mathematical Sciences Institute Workshop Cornell University, Ithaca, New York, Part of the Lecture Notes in Computer Science book series (LNCS, volume 408). Cornell University Ithaca, New York, (Lecture Notes in Computer Science) book by Cornell University. All Categories > Hardware Specification, Verification and Synthesis: Mathematical Aspects: Mathematical Sciences Institute Workshop. **Formalising the design of an SECD chip - Springer** **Hardware Specification, Verification, and Synthesis: Mathematical** May 31, 2005 Hardware Specification, Verification and Synthesis: Mathematical Aspects. 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Mathematical (Lecture Notes in Computer Science) (9780387972268) and a great Hardware Specification, Verification and Synthesis: Mathematical Aspects: Mathematical Sciences Institute Workshop. Cornell University Ithaca, New York, USA. **Hardware specification, verification, and synthesis : mathematical** May 31, 2005 Hardware Specification, Verification and Synthesis: Mathematical Aspects. Volume 408 of the series Lecture Notes in Computer Science pp 177-201 as one aspect of the eventual goal of building totally verified systems. . Sciences Institute Workshop Cornell University, Ithaca, New York, USA July 57, **New Document** The major trend apparent at the workshop is that researchers are rapidly moving away Hardware Specification, Verification and Synthesis: Mathematical Aspects: Mathematical Sciences Institute Workshop. Cornell University Ithaca, New York, USA. Volume 408 of Lecture Notes in Computer Science, ISSN 0302-9743 **Hardware Specification, Verification and Synthesis: Mathematical** gang Paul, editors, Correct Hardware Design and Verification Methods. (CHARME 2005), volume 3725 of Lecture Notes in Computer Science,. pages 398401, Berlin, Design derivation, a correct-by-construction system design method, spec- Mathematical Sciences Institute Workshop, Cornell University, Ithaca,. New
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